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APPLICATION NO). 1	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/832,844		04/12/2001	Shunpei Yamazaki	740756-2288	2849
31780	7590	02/25/2005		EXAM	INER
ERIC ROBINSON			TRAN, MINH LOAN		
PMB 955					
21010 SOU	JTHBANK	KST.		ART UNIT	PAPER NUMBER
POTOMA	C FALLS,	VA 20165		2826	•
				DATE MAILED: 02/25/2005	5

Please find below and/or attached an Office communication concerning this application or proceeding.

Attachment(s)

1)	M	Notice of	References	Cited (PTO-892)
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2) Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 10/29/04;10/15/04;6/17/04

4) 🔲	Interview Summary (PTO-413)
_	Paper No(s)/Mail Date

5) Notice of Informal Patent Application (PTO-152)

6)	 Other:

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DETAILED ACTION

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 12/20/2004 has been entered.

Information Disclosure Statement

2. The information disclosure statements filed 10/29/04, 10/15/04, 06/17/04 have been considered.

Claim Rejections - 35 USC § 112

3. Claims 37 and 38 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claims 37 and 38 are redundant of claims 33 and 34.

Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

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Claims 33, 34, 37, 38 are rejected under 35 U.S.C. 103(a) as being unpatentable over Takashi (JP 02-27320).

Figures 1-3 of Takashi disclose a liquid crystal display device comprising an insulating substrate 1; a plurality of first lines extending in a first direction over the substrate 1; a plurality of second lines extending across the first lines over the substrate 1; a plurality of pixels defined by the first lines and second lines in the region 4; a plurality of pixel electrodes provided at the pixels (figure 3); a plurality of switching elements provided at the pixels, each of the switching elements comprising at least one first thin film transistor wherein a gate electrode 7 of the first thin film transistor is electrically connected to one of the plurality of first lines (figure 3); an interlayer insulating film 8 formed over the plurality of switching element wherein the pixel electrodes are provided over the interlayer insulating film 8 (lines 15, 16 on page 4 of the English translation); a first gate driver circuit 2 formed over the substrate 1 and electrically connected to the plurality of first line (figure 3), the first gate driver circuit 2 comprising a plurality of second thin film transistors; a second driver circuit 3 comprising an IC chip electrically connected to the plurality of second lines.

Takashi does not disclose the interlayer insulating film comprises of resin.

However, it would have been obvious to one of ordinary skill in the art to form the interlayer insulating film of Takashi's device comprises of resin because such material is conventional in the art for forming the interlayer insulating film or the passivation film for the thin film transistors.

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Allowable Subject Matter

5. Claims 7-18, 26-32, 35, 36, 39-54 are allowable over the references of record because none of these references disclose or can be combined to yield the claimed invention such as a plurality of switching elements provided at the pixels, each of the switching elements comprising a first thin film transistor; a driver circuit comprising a plurality of second thin film transistors; wherein all of the first thin film transistors and the second thin film transistors are NTFTs or PTFTs.

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Minh-Loan T. Tran whose telephone number is (571) 272-1922. The examiner can normally be reached on Monday-Friday 9:00 AM-5:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan J. Flynn can be reached on (571) 272-1915. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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MIt 02/2005 Minh-Loan T. Tran Primary Examiner Art Unit 2826